As a result of the exploding increase in CPU complexity according to Moore’s Law, all modern processors require a low voltage with high instantaneous current power source. Voltage Regulator Modules, along with embedded solutions, are the only way to correctly supply these currents. A need to simulate this low voltage, high current with fast di/dt loading has become more important than ever. Every main board design using a VRM, Intel® Itanium™ power pod or an embedded regulator (VRD) will need processor voltage supply performance qualification.

Integrating Intel®’s latest high di/dt test technology with years of experience in the design and manufacture of highly accurate, precision DC load and measurement instrumentation, the Chroma 63472 electronic load is your total solution for qualifying all main board Intel® processor power performance requirements!

The Chroma 63472 electronic load provides 150A of static, DC load with 150A of dynamic load - featuring up to 1000A/ μs slew rate! This fully controllable capability, along with Dynamic VID control and sophisticated efficiency, timing, V/I measurements functions, comprises a total solution - all in one unit. Gone is the need for the individual equipment stack of traditional test setups, saving precious bench space and expense. The Chroma 63472 can replace a function generator, oscilloscope, DVM, DC load bank, in addition to any customized dynamic load and VID control circuitry.

Similarly, the Chroma 63472 makes an economical choice for all your production line test requirements. From industry data, more than 5% of today’s main boards fail due to on-board converter failures. The Chroma 63472 can quickly identify suspect units in production - well ahead of an expensive service call.
1. Incorporates Intel®’s latest Processor Power Test Tool (PTT)

Using new slew rate and loading control techniques, Intel® recently introduced the latest generation PTT to address ultra-fast processor di/dt requirements.

The PTT is controllable by external analog and clock signals. It will sink step currents up to 150A at slew rates over 1000A/μs, with repetition rates up to 1MHz. Users may drive this component with a traditional DAQ card and/or signal generator, or, simply choose to use the Chroma 63472 to obtain a complete test solution.

2. Auto Calibration

Because the PTT is an “open loop” design, a constant calibration scheme for assuring accurate loading levels and slew rates is necessary. The Chroma 63472 has built-in means for accomplishing real-time, on-the-fly calibrations. Using a high speed ADC and comparators, all necessary calibrations are automatically performed before any measurement reading is taken to ensure accuracy.

3. Static Loading Simulation

In order to physically fit into all current and future CPU sockets, the PTT heat sink volume is small and therefore cannot reliably dissipate the huge amount of heat generated when loading a processor power source continuously.

The Chroma 63472 has a built-in 200W DC electronic load for converter design / development, thermal studies, and reliability testing. It can sink its full rated current down to 0.5V, exceeding all existing processor voltage requirements.

4. Efficiency Test

Real time measurement of UUT input/output voltage and current is integrated in the 63472 control and measurement unit using 16 bits precision measurement circuit, user can easily get the UUT efficiency.

\[ \text{Efficiency} = \frac{\text{Vout} \times \text{Iout}}{\text{Vin} \times \text{Iin}} \times 100\% \]

5. PWRGD Timing

With the unique timing measurement of UUT power good signal. User can setting the start trigger (in %) of Vout voltage and end trigger level of PWRGD signal to get PWRGD timing.
6. VID Control / Dynamic Simulation

The Chroma 63472 provides two built-in VID tables for the VRM10.X and VRM9.X series. There are also three user definable VID tables that allow the user to expand for future VID coding schemes.

For the VRM10.X series regulator, a unique dynamic VID function needs to be tested. The Chroma 63472 allows a user to specify any number of VID steps within the programmed VID table. In addition, the step time may be reduced to as little as 5 µs. Measurement of the output voltage behavior throughout the entire Dynamic VID sequence is supported.

7. General Measurements

The Chroma 63472’s comprehensive measurement capability provides all the necessary voltage (Vpk+ and Vpk-) and current information for both static and dynamic CPU “load-line” measurements.

In the Dynamic VID simulation mode, the measurement system captures the peak voltage reading and compares it with the Dynamic VID specification limits. Any value found outside of the boundaries is flagged as a failure.

UUT output voltage and current readings are measured through two separate SMA connectors on the unit’s front panel. This provides the user with the flexibility to locate the most desirable voltage measurement point on any particular main board. Doing so will maximize accuracy of the measurement values and minimize external noise interference.

8. Embedded VRD Testing

Customized designed adapter boards are available for most popular Intel®’s CPU sockets such like sockets 478, 604 etc. This enable not only system integrator or M/B makers to perform full functional test against VRM on board or VRD.

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**Applications**

- **VRM / Power Pod Testing**
- **PC Mother Board Testing**
- **R / D Validation And Characterization Test**
- **System Integration And Production Line Test**
## SPECIFICATIONS

### Static Load Section

<table>
<thead>
<tr>
<th><strong>POWER</strong></th>
<th>200W</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>CURRENT</strong></td>
<td>Range 0 ~ 150A</td>
</tr>
<tr>
<td>Resolution</td>
<td>12 Bits</td>
</tr>
<tr>
<td>Accuracy</td>
<td>0.1% + 0.2% F.S. (typical)</td>
</tr>
<tr>
<td><strong>VOLTAGE</strong></td>
<td>Range 0 ~ 2V</td>
</tr>
<tr>
<td>Min. voltage</td>
<td>0.5V @ full load</td>
</tr>
</tbody>
</table>

### Dynamic Load Section

| **OCP** | 165A |
| **OVP** | 2.1V |
| **Current Range** | 0 ~ 150A |
| **Dynamic-clock** | 1MHz (10 ~ 90%) |
| **Resolution** | 1% |
| **Accuracy** | 100 ppm |
| **Slew Rate** | 100A/s ~ 1000A/s |
| **Min. rise time** | 100ns |

### TTL Output

- **Level**: Low (<0.8V), High (>3.15V)
- **VID code**: 6 bits
- **QUTEN**: Yes

### Aux. Power

- **Outputs**: +12V / +5V
- **Current**: 0.5A / 0.3A
- **Power**: 7.5W total
- **Load regulation**: 5%

### Measurement Section

#### Input Section

- **Input Voltage read back**: Range 15V / 58V (Auto)
- **Resolution**: 16 bits
- **Accuracy**: 0.05% + 0.05% F.S.

- **Input Current read back**: Range 0.5V (refer to 5A) / 2.5V (refer to 2.5A) (Auto)
- **Resolution**: 16 bits
- **Accuracy**: 0.1% + 0.1% F.S.

### Static Mode

- **Voltage read back**: Range 0 ~ 2V
- **Resolution**: 16 bits
- **Accuracy**: 0.05% + 0.05% F.S.

### Dynamic Mode

- **Current read back**: Range 0 ~ 150A
- **Resolution**: 16 bits
- **Accuracy**: 0.1% + 0.1% F.S.

- **Peak Voltage**: Range 0 ~ 2V
- **Resolution**: 12 bits
- **Accuracy**: 0.5% + 0.2% F.S.

### Dynamic VID

- **Voltage read back**: Range 0 ~ 2V
- **Resolution**: 12 bits
- **Accuracy**: 0.5% + 0.2% F.S.

### Dynamic VID Control

- **Dwell time**: Range 5 μs ~ 10ms
- **Resolution**: 1 μs

### Power Good Timing Measurement Section

- **Range**: 1S
- **Resolution**: 1μS
- **Accuracy**: 5μS

### Other Interface

- **RS-232**: For A634701 Remote Controller or PC Control
- **GPIB**: Optional
- **EMCASAFETY**: CE

### Ordering Information

- **63472**: High Slew Rate Load Control & Measurement Unit
- **634701**: Remote controller for 63472
- **634702**: Transient Load
- **634703**: Test fixture for Power Pod
- **634704**: Test fixture for VRM10.X
- **634705**: Socket 478 Interposer Card
- **634706**: Socket 604 Interposer Card of Single Voltage
- **634707**: Socket 604 Interposer Card of Dual Voltage
- **634708**: Socket 604 Interposer Card of Triple Voltage
- **634709**: 19” Rack Mounting Kit
- **634710**: Test fixture for MVR Rev 0.8
- **634001**: GPIB Option Board

*All specifications are subject to change prior notice.*

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